

# Low-Latency Ordered Statistics Decoding of BCH Codes

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**Abstract**—This paper proposes a low-latency ordered statistics decoding (OSD) algorithm for BCH codes. The OSD latency is mainly caused by Gaussian elimination (GE) that produces a systematic generator matrix of the code. Considering BCH codes is binary subcodes of Reed-Solomon (RS) codes, we show that the BCH codeword candidates can be produced through the systematic generator matrix of the corresponding RS code. The systematic generator matrix of an RS code can be formed by generating the linearly independent RS codewords in parallel, replacing the GE process and enabling a low OSD latency. This paper further proposes a segmented variant that facilitates the decoding by reducing the number of test error patterns (TEPs). Complexity of the proposed OSD is also analyzed. Our simulation results show that the proposed decoding can achieve a similar performance as the conventional OSD, but with a lower decoding complexity. The decoding latency can be reduced over the conventional OSD substantially.

**Index Terms**—BCH codes, low-latency, subfield subcode, maximum likelihood decoding, ordered statistics decoding

## I. INTRODUCTION

The realization of ultra-reliable low-latency communication (URLLC) requires the support of competent short-to-medium length channel codes. The transmission limit of a finite length coded system has been characterized in [1]. Recent research on short-to-medium length codes has shown that ordered statistics decoding (OSD) of BCH codes can yield a performance that is closed to the transmission limit [2]–[3]. In OSD, the codeword candidates are generated through the re-encoding of test messages that are formed by alternating decisions of the most reliable independent positions (MRIPs) in a codeword. The re-encoding process requires Gaussian elimination (GE) that produces a systematic generator matrix of the code. However, due to the sequential feature of GE, its latency cannot be compromised, which is also a long-standing challenge for OSD [4]. In order to reduce the OSD complexity, several skipping and stopping rules have been proposed in [5]–[8]. They facilitate the decoding by identifying the unpromising test error patterns (TEPs) and the maximum likelihood (ML) codeword candidate within the decoding output list, respectively. They result in skipping the unpromising TEPs, or terminating the decoding earlier. The box-and-match algorithm [9] trades time and space complexity by considering the TEPs of small weights. Moreover, the MRIPs segmentation approach was proposed in [10], dividing the OSD operation into several segments to reduce the decoding complexity. On the other aspect, the multiple

information sets generated by randomly biased log-likelihood ratios (LLRs) were proposed in [11]–[12] in order to improve the OSD performance.

However, the GE latency challenge remains, which will be addressed by this work. Since BCH codes are binary subcodes of Reed-Solomon (RS) codes, their codeword candidates can be generated through the corresponding RS codewords, which requires the RS systematic generator matrix. It can be formed by generating the linearly independent RS codewords in parallel, underpinning a low decoding latency. In particular, an  $(n, k)$  BCH code is a binary subcode of an  $(n, k')$  RS code that is defined over a binary extension field, where  $n$  is their codeword length and the dimension of the RS code is greater than that of the BCH code, i.e.,  $k' > k$ . The  $k'$  linearly independent RS codewords can be generated in parallel using the Lagrange interpolation polynomials, forming the RS systematic generator matrix. The BCH codeword candidates can be yielded through generating the binary RS codewords by the matrix. In order to further reduce the decoding complexity, a segmented low-latency OSD is further proposed. By segmenting the original TEPs, a near ML decoding performance can still be achieved with less TEPs, resulting in a lower decoding complexity. Complexity of the proposed OSD is analyzed. Our simulation results show that the decoding latency (in microsecond) can be substantially reduced over the conventional OSD. They yield a similar decoding performance as the conventional OSD with a smaller decoding output list, resulting in fewer floating point operations for identifying the most likely codeword from the list.

## II. PRELIMINARIES

### A. Ordered Statistics Decoding

Let  $\mathbb{F}_q$  denote a finite field of size  $q$ , and its extension field is further denoted as  $\mathbb{F}_{q^m}$ , where  $m > 1$ . Let  $\underline{\mathbf{f}} = (f_0, f_1, \dots, f_{k-1}) \in \mathbb{F}_2^k$  and  $\underline{\mathbf{c}} = (c_0, c_1, \dots, c_{n-1}) \in \mathbb{F}_2^n$  denote the message vector and codeword vector of an  $(n, k)$  BCH code, respectively, and  $d$  denote its minimum Hamming distance. Its generator matrix  $\mathbf{G}$  is a  $k \times n$  binary matrix as  $\mathbf{G} = [\mathbf{g}_0, \mathbf{g}_1, \dots, \mathbf{g}_{n-1}]$ , where  $\mathbf{g}_0, \mathbf{g}_1, \dots, \mathbf{g}_{n-1}$  are the column vectors of length  $k$ . Let us assume that a BCH codeword  $\underline{\mathbf{c}}$  is transmitted by the use of BPSK modulation as  $: 0 \mapsto 1; 1 \mapsto -1$ . The modulated symbol sequence is  $\underline{\mathbf{x}} = (x_0, x_1, \dots, x_{n-1})$ , where  $x_j \in \{-1, 1\}$  and  $j = 0, 1, \dots, n-1$ . After a memoryless channel, the

received symbol sequence is  $\underline{r} = (r_0, r_1, \dots, r_{n-1}) \in \mathbb{R}^n$ . Let  $\Pr(r_j | c_j = 0)$  and  $\Pr(r_j | c_j = 1)$  denote channel observations of  $c_j$ , its received LLR is defined as

$$L_j = \ln \frac{\Pr(r_j | c_j = 0)}{\Pr(r_j | c_j = 1)}. \quad (1)$$

Subsequently, the hard-decision received word  $\underline{y} = (y_0, y_1, \dots, y_{n-1}) \in \mathbb{F}_2^n$  can be obtained. That says if  $L_j > 0$ ,  $y_j = 0$ ; otherwise,  $y_j = 1$ . Since a greater  $|L_j|$  indicates the received information of  $c_j$  is more reliable, reliability of the received information for all coded bits can be ordered based on  $|L_j|$ , yielding a refreshed bit index sequence  $j_0, j_1, \dots, j_{n-1}$ . It indicates  $|L_{j_0}| \geq |L_{j_1}| \geq \dots \geq |L_{j_{n-1}}|$ . A permuted received word can be further obtained as

$$\underline{y}' = \Pi(\underline{y}) = (y_{j_0}, y_{j_1}, \dots, y_{j_{n-1}}), \quad (2)$$

where  $\Pi$  denotes the permutation function. Applying the same permutation to the columns of  $\mathbf{G}$  yields

$$\mathbf{G}' = \Pi(\mathbf{G}) = [\mathbf{g}_{j_0}, \mathbf{g}_{j_1}, \dots, \mathbf{g}_{j_{n-1}}]. \quad (3)$$

GE will be performed on  $\mathbf{G}'$ , reducing columns  $\mathbf{g}_{j_0}, \mathbf{g}_{j_1}, \dots, \mathbf{g}_{j_{k-1}}$  into weight one and yielding a systematic generator matrix as

$$\mathbf{G}'' = [\mathbf{g}'_{j_0}, \mathbf{g}'_{j_1}, \dots, \mathbf{g}'_{j_{n-1}}], \quad (4)$$

where columns  $\mathbf{g}'_{j_0}, \mathbf{g}'_{j_1}, \dots, \mathbf{g}'_{j_{k-1}}$  form a  $k \times k$  identity submatrix. However, this cannot be achieved if the first  $k$  columns are not linearly independent. In this case, a second permutation will be needed, and the GE will be conducted again. This adjustment continues until the first  $k$  columns of  $\mathbf{G}'$  are linearly independent. Note that if a second permutation is needed,  $\underline{y}'$  will also be updated accordingly. Without further mentioning, we assume that the first  $k$  columns of  $\mathbf{G}'$  have been ensured with this property.

Consequently, after ensuring the first  $k$  columns of  $\mathbf{G}'$  being linearly independent, the first  $k$  positions in  $\underline{y}'$  are called the MRIPs and their index set is denoted as  $\Upsilon = \{j_0, j_1, \dots, j_{k-1}\}$ . Let  $\underline{f} = (y_{j_0}, y_{j_1}, \dots, y_{j_{k-1}})$  denote a message and  $\underline{e}^{(\omega)} = (e_{j_0}^{(\omega)}, e_{j_1}^{(\omega)}, \dots, e_{j_{k-1}}^{(\omega)}) \in \mathbb{F}_2^k$  denote a TEP that will be used to update  $\underline{f}$ , where  $\omega = 1, 2, \dots, \sum_{\lambda=0}^{\tau} \binom{k}{\lambda}$ . For each  $\underline{e}^{(\omega)}$ , there are at most  $\tau$  non-zero entries. The test messages can be generated by

$$\underline{f}^{(\omega)} = \underline{f} + \underline{e}^{(\omega)}. \quad (5)$$

The corresponding codeword candidate can be generated by

$$\hat{\underline{c}}^{(\omega)} = (\hat{c}_0^{(\omega)}, \hat{c}_1^{(\omega)}, \dots, \hat{c}_{n-1}^{(\omega)}) = \Pi^{-1}(\underline{f}^{(\omega)} \cdot \mathbf{G}''), \quad (6)$$

where  $\hat{\underline{c}}^{(\omega)} \in \mathbb{F}_2^n$  and  $\Pi^{-1}$  is the inverse of the permutation function  $\Pi$ . Let us further define the correlation distance between  $\underline{y}$  and  $\hat{\underline{c}}^{(\omega)}$  as

$$d(\underline{y}, \hat{\underline{c}}^{(\omega)}) \triangleq \sum_{j: y_j \neq \hat{c}_j^{(\omega)}} |L_j|. \quad (7)$$

A codeword candidate with a smaller correlation distance to  $\underline{y}$  is more likely to be the transmitted codeword. Let  $S_\omega = \{L_j | y_j = \hat{c}_j^{(\omega)}\}$ , elements  $L_j$  of  $S_\omega$  can be reordered as

$$|L_{\xi_0}| \leq |L_{\xi_1}| \leq \dots \leq |L_{\xi_{(n-d_\omega-1)}}|, \quad (8)$$

where  $d_\omega$  denotes the Hamming distance between  $\underline{y}$  and  $\hat{\underline{c}}^{(\omega)}$ . The ML criterion is [5]

$$d(\underline{y}, \hat{\underline{c}}^{(\omega)}) \leq \sum_{j=0}^{d-d_\omega-1} |L_{\xi_j}|. \quad (9)$$

If  $\hat{\underline{c}}^{(\omega)}$  satisfies (9), it will be the ML codeword. The OSD decoding can be terminated once the ML codeword is found. Otherwise, the one that yields the smallest correlation distance to  $\underline{y}$  will be selected as the decoding output  $\hat{\underline{c}}_{\text{opt}}$ .

Note that the GE that produces the systematic generator matrix  $\mathbf{G}''$  is a sequential process incurring the OSD latency challenge.

### B. BCH Codes and RS Codes

The subfield subcode relationship between BCH codes and RS codes is stated as follows.

**Definition 1** ([13]): Given two linear block codes  $\mathcal{C}$  and  $\mathcal{C}'$  of length  $n$ , they are defined over  $\mathbb{F}_q$  and  $\mathbb{F}_{q^m}$ , respectively. If  $\mathcal{C} = \mathcal{C}' \cap \mathbb{F}_q^n$ ,  $\mathcal{C}$  is a subcode of  $\mathcal{C}'$  over  $\mathbb{F}_q$ .

**Lemma 1** ([14]): An  $(n, k)$   $t$  error-correcting BCH code defined over  $\mathbb{F}_2$  is a subcode of an  $(n, k')$   $t$  error-correcting RS code defined over  $\mathbb{F}_{2^m}$ . Note that, the RS codes are the maximum distance separable (MDS) codes. With the same error correction capacity, the RS code dimension is greater than that of the BCH subcode, i.e.,  $k' > k$ .

## III. LOW-LATENCY ORDERED STATISTICS DECODING

### A. RS Systematic Generator Matrix

With the permuted received word  $\underline{y}'$  of (2), let us define  $\Theta = \{j_0, j_1, \dots, j_{k'-1}\}$  as the index set of its  $k'$  most reliable positions (MRPs), and its complementary set  $\Theta^c = \{j_{k'}, j_{k'+1}, \dots, j_{n-1}\}$ . Note that since the OSD is discussed under the binary BCH code paradigm, it is assumed  $\underline{y}' \in \mathbb{F}_2^n$ . Otherwise, for an RS code,  $\underline{y}' \in \mathbb{F}_{2^m}^n$ . Picking up the received symbols indexed by  $\Theta$ , an initial message  $\underline{u} = (y_{j_0}, y_{j_1}, \dots, y_{j_{k'-1}}) \in \mathbb{F}_{2^m}^{k'}$  can be formed. We also denote the support of its symbol indices that are realized in  $\underline{y}'$  as  $\text{supp}(\underline{u}) = \{j_0, j_1, \dots, j_{k'-1}\}$ . With  $\underline{u}$ , the message polynomial of the  $(n, k')$  RS code can be defined as

$$H_{\underline{u}}(x) = \sum_{j \in \text{supp}(\underline{u})} y_j L_j(x), \quad (10)$$

where

$$L_j(x) = \prod_{j' \in \text{supp}(\underline{u}), j' \neq j} \frac{x - \alpha_{j'}}{\alpha_j - \alpha_{j'}} \quad (11)$$

is the Lagrange interpolation polynomial of code locator  $\alpha_j$ . It enables  $L_j(\alpha_j) = 1$ , and  $L_j(\alpha_{j'}) = 0$  if  $j' \neq j$ . With code locators  $\alpha_0, \alpha_1, \dots, \alpha_{n-1}$ , the RS codeword  $\underline{v} = (v_0,$

$v_1, \dots, v_{n-1}) \in \mathbb{F}_2^n$  can be generated by

$$\underline{v} = (H_{\underline{u}}(\alpha_0), H_{\underline{u}}(\alpha_1), \dots, H_{\underline{u}}(\alpha_{n-1})). \quad (12)$$

Let us define  $k'$  weight-1 messages as  $\underline{u}_{j_0} = (1, 0, \dots, 0)$ ,  $\underline{u}_{j_1} = (0, 1, \dots, 0)$ ,  $\dots$ ,  $\underline{u}_{j_{k'-1}} = (0, 0, \dots, 1)$ , respectively. They have the same support as  $\underline{u}$ , i.e.,  $\text{supp}(\underline{u}_{j_0}) = \text{supp}(\underline{u}_{j_1}) = \dots = \text{supp}(\underline{u}_{j_{k'-1}}) = \Theta$ . Consequently, the generator matrix of the  $(n, k')$  RS code can be generated by

$$\mathbf{G}_{\text{RS}} = \begin{bmatrix} H_{\underline{u}_{j_0}}(\alpha_0) & H_{\underline{u}_{j_0}}(\alpha_1) & \cdots & H_{\underline{u}_{j_0}}(\alpha_{n-1}) \\ H_{\underline{u}_{j_1}}(\alpha_0) & H_{\underline{u}_{j_1}}(\alpha_1) & \cdots & H_{\underline{u}_{j_1}}(\alpha_{n-1}) \\ \vdots & \vdots & \ddots & \vdots \\ H_{\underline{u}_{j_{k'-1}}}(\alpha_0) & H_{\underline{u}_{j_{k'-1}}}(\alpha_1) & \cdots & H_{\underline{u}_{j_{k'-1}}}(\alpha_{n-1}) \end{bmatrix}, \quad (13)$$

where each row is a codeword of the respective message. Since the  $k'$  messages are linearly independent, the  $k'$  codewords are also linearly independent. They constitute the generator matrix of the  $(n, k')$  RS code. In  $\mathbf{G}_{\text{RS}}$ , columns  $j_0, j_1, \dots, j_{k'-1}$  form a  $k' \times k'$  identity submatrix. Hence,  $\mathbf{G}_{\text{RS}}$  is in the systematic form. The row- $i$  column- $j$  entry of  $\mathbf{G}_{\text{RS}}$  is

$$H_{\underline{u}_i}(\alpha_j) = \begin{cases} 0, & \text{if } j \in \Theta, j \neq i; \\ 1, & \text{if } j \in \Theta, j = i; \\ \frac{\prod_{j' \in \Theta} (\alpha_j - \alpha_{j'})}{(\alpha_j - \alpha_i) \prod_{j' \in \Theta, j' \neq i} (\alpha_i - \alpha_{j'})}, & \text{if } j \in \Theta^c. \end{cases} \quad (14)$$

Since  $\alpha_j \prod_{j'=0, j' \neq j}^{n-1} (\alpha_j - \alpha_{j'}) = 1$ , when  $j \in \Theta^c$ ,

$$H_{\underline{u}_i}(\alpha_j) = \frac{\alpha_i \prod_{j' \in \Theta^c} (\alpha_i - \alpha_{j'})}{\alpha_j (\alpha_j - \alpha_i) \prod_{j' \in \Theta^c, j' \neq j} (\alpha_j - \alpha_{j'})}. \quad (15)$$

Note that when the code rate is greater than 1/2,  $|\Theta^c| < |\Theta|$ , and eq. (15) requires less finite field computation.

Note that matrix  $\mathbf{G}_{\text{RS}}$  can be generated in parallel, underpinning the low-latency feature of the proposed OSD.

### B. Generation of BCH Codeword Candidates

The BCH codeword candidates can be further generated by  $\mathbf{G}_{\text{RS}}$ . With the initial message  $\underline{u} = (y_{j_0}, y_{j_1}, \dots, y_{j_{k'-1}})$ , RS codeword  $\hat{\underline{v}}^{(0)} = (\hat{v}_0^{(0)}, \hat{v}_1^{(0)}, \dots, \hat{v}_{n-1}^{(0)}) \in \mathbb{F}_2^n$  can be generated by

$$\hat{\underline{v}}^{(0)} = \underline{u} \cdot \mathbf{G}_{\text{RS}}, \quad (16)$$

where  $\hat{v}_j^{(0)} = y_j, \forall j \in \Theta$ . Similar to the OSD that was introduced in Section II. A, let us also define a TEP as  $\underline{e}^{(\omega)} = (e^{(\omega)}_{j_0}, e^{(\omega)}_{j_1}, \dots, e^{(\omega)}_{j_{k'-1}}) \in \mathbb{F}_2^{k'}$ . Subsequently, the test message  $\underline{u}^{(\omega)}$  can be generated by

$$\underline{u}^{(\omega)} = \underline{u} + \underline{e}^{(\omega)}. \quad (17)$$

The corresponding RS codeword  $\hat{\underline{v}}^{(\omega)} = (\hat{v}_0^{(\omega)}, \hat{v}_1^{(\omega)}, \dots, \hat{v}_{n-1}^{(\omega)})$  can be further generated by

$$\begin{aligned} \hat{\underline{v}}^{(\omega)} &= (\underline{u} + \underline{e}^{(\omega)}) \cdot \mathbf{G}_{\text{RS}} \\ &= \hat{\underline{v}}^{(0)} + \underline{e}^{(\omega)} \cdot \mathbf{G}_{\text{RS}}, \end{aligned} \quad (18)$$

where  $\hat{\underline{v}}^{(\omega)} \in \mathbb{F}_2^n$ . Based on *Lemma 1*, if  $\hat{\underline{v}}^{(\omega)} \in \mathbb{F}_2^n$ , it is also an  $(n, k)$  BCH codeword. The following Theorem shows that this binary assessment can be implemented effectively by knowing  $\hat{\underline{v}}^{(0)}$ ,  $\underline{e}^{(\omega)}$  and  $\mathbf{G}_{\text{RS}}$ .

**Theorem 2:** If  $\hat{v}_j^{(0)} + \sum_{i \in \Theta, e^{(\omega)}_i \neq 0} H_{\underline{u}_i}(\alpha_j)$  is binary, for all  $j \in \Theta^c$ ,  $\hat{\underline{v}}^{(\omega)}$  is a BCH codeword.

*Proof:* Based on (18), let us define

$$\underline{e}^{(\omega)} \cdot \mathbf{G}_{\text{RS}} = (\phi_0^{(\omega)}, \phi_1^{(\omega)}, \dots, \phi_{n-1}^{(\omega)}). \quad (19)$$

The RS codeword symbol  $\hat{v}_j^{(\omega)}$  can be determined by

$$\hat{v}_j^{(\omega)} = \hat{v}_j^{(0)} + \phi_j^{(\omega)}. \quad (20)$$

Based on (14), we know if  $j \in \Theta$ ,

$$\phi_j^{(\omega)} = e^{(\omega)}_j. \quad (21)$$

Since for  $j \in \Theta$ ,  $\hat{v}_j^{(0)} \in \{0, 1\}$  and the TEP  $\underline{e}^{(\omega)}$  is also binary. Hence,  $\hat{v}_j^{(\omega)} \in \{0, 1\}, \forall j \in \Theta$ . For the remaining symbols with index  $j \in \Theta^c$ , based on (14) and (18), we know

$$\hat{v}_j^{(\omega)} = \hat{v}_j^{(0)} + \sum_{i \in \Theta, e^{(\omega)}_i \neq 0} H_{\underline{u}_i}(\alpha_j). \quad (22)$$

Therefore, if they are binary, codeword  $\hat{\underline{v}}^{(\omega)}$  is binary. Based on *Lemma 1*, it is also a BCH codeword. ■

Similar to the conventional OSD, the proposed OSD generates the codeword candidates by numerating the TEPs  $\underline{e}^{(\omega)}$  and re-encoding as in (18). Based on *Theorem 2*, if codeword symbols  $\hat{v}_j^{(\omega)} (j \in \Theta^c)$  are binary,  $\hat{\underline{v}}^{(\omega)}$  will be a BCH codeword. The correlation distance between  $\underline{y}$  and  $\hat{\underline{v}}^{(\omega)}$  will be further determined as in (7). Once a codeword candidate  $\hat{\underline{v}}^{(\omega)}$  satisfies the ML criterion of (9),  $\hat{\underline{v}}^{(\omega)}$  will be selected as the decoding output  $\hat{\underline{v}}_{\text{opt}}$  and decoding terminates. Otherwise, the one that yields the smallest correlation distance with  $\underline{y}$  will be selected as  $\hat{\underline{v}}_{\text{opt}}$ .

Since the systematic generator matrix  $\mathbf{G}_{\text{RS}}$  can be generated in parallel, it yields a decoding latency advantage over the conventional OSD. Summarizing the above description, the low-latency OSD is shown below as in *Algorithm 1*.

## IV. SEGMENTED VARIANT

This section further proposes a segmented variant of the proposed OSD, in order to reduce its complexity.

The above description shows that in the OSD, if the number of errors in the MRIPs is not greater than the decoding order  $\tau$ , the transmitted codeword will be included in the decoding output list. Let  $P_{e, \text{OSD}}(\tau)$ ,  $P_{e, \text{ML}}$  and  $P_e(\tau)$  denote the error probability of OSD with an order  $\tau$ , the error probability of the ML decoding, and the probability that the number of errors in the MRIPs is greater than  $\tau$ , respectively. They hold

$$P_{e, \text{OSD}}(\tau) \leq P_{e, \text{ML}} + P_e(\tau). \quad (23)$$

**Algorithm 1** Low-Latency OSD of BCH Codes**Input:** Received symbol sequence  $\underline{r}$ , order  $\tau$ ;**Output:**  $\hat{\underline{v}}_{\text{opt}}$ ;

- 1: Compute the LLRs as in (1), and determine  $\underline{y}$ ;
- 2: Define MRPs,  $\underline{u}$ , and let  $d_{\min} = +\infty$ ;
- 3: Generate  $\mathbf{G}_{\text{RS}}$  as in (14);
- 4: Generate the initial codeword  $\hat{\underline{v}}^{(0)}$  as in (16);
- 5: **For** each TEP  $\underline{e}^{j(\omega)}$ , **do**
- 6:   Test if the codeword  $\hat{\underline{v}}^{(\omega)}$  is binary as in (22);
- 7:   **If**  $\hat{\underline{v}}^{(\omega)}$  is binary
- 8:     Determine  $d(\underline{y}, \hat{\underline{v}}^{(\omega)})$  as in (7);
- 9:     **If**  $d(\underline{y}, \hat{\underline{v}}^{(\omega)}) < d_{\min}$
- 10:      Update  $d_{\min} = d(\underline{y}, \hat{\underline{v}}^{(\omega)})$  and  $\hat{\underline{v}}_{\text{opt}} = \hat{\underline{v}}^{(\omega)}$ ;
- 11:     **If**  $d(\underline{y}, \hat{\underline{v}}^{(\omega)})$  satisfies the ML criterion of (9)
- 12:      Terminate the decoding;
- 13: **End for**
- 14: Return  $\hat{\underline{v}}_{\text{opt}}$ ;

When

$$\tau \geq \min \left\{ \left\lceil \frac{d}{4} - 1 \right\rceil, k \right\}, \quad (24)$$

$P_{\underline{e}}(\tau) \ll P_{e,\text{ML}}$  [3]. Therefore, if the OSD order is sufficiently large, it can approach the ML decoding performance.

Since the length of TEP  $\underline{e}^{j(\omega)}$  is greater than that of  $\underline{e}^{j(\omega)}$  in the conventional OSD, there are more test errors in  $\underline{e}^{j(\omega)}$ . They occur in the extra symbol band defined by  $\Theta \setminus \Upsilon$ . The analysis of [3] shows that if  $\tau$  satisfies (24),  $P_{\underline{e}}(\tau)$  becomes negligible. Hence, there is no need to assign an order greater than  $\tau$  for the first  $k$  positions of MRPs.

With  $\underline{e}^{j(\omega)}$ , we can partition it into two segments as  $\underline{e}_1^{j(\omega)} = (e'_{j_0}, e'_{j_1}, \dots, e'_{j_{k-1}})$  and  $\underline{e}_2^{j(\omega)} = (e'_{j_k}, e'_{j_{k+1}}, \dots, e'_{j_{k'-1}})$ , respectively. The proposed OSD can be performed by numerating  $\underline{e}_1^{j(\omega)}$  and  $\underline{e}_2^{j(\omega)}$ , which form a smaller set of TEPs. Let  $\tau_1$  and  $\tau_2$  denote the segment orders of  $\underline{e}_1^{j(\omega)}$  and  $\underline{e}_2^{j(\omega)}$ , respectively. Similar to the definition of  $P_{\underline{e}}(\tau)$ , let  $P_{\underline{e}_1}(\tau_1)$  and  $P_{\underline{e}_2}(\tau_2)$  denote the probabilities of the number of errors in  $\underline{e}_1^{j(\omega)}$  is greater than  $\tau_1$  and the number of errors in  $\underline{e}_2^{j(\omega)}$  is greater than  $\tau_2$ , respectively. In a memoryless channel, we have

$$P_{\underline{e}}(\tau) = 1 - (1 - P_{\underline{e}_1}(\tau_1))(1 - P_{\underline{e}_2}(\tau_2)). \quad (25)$$

Based on (23), we can obtain the error probability upper bound of the segmented OSD as

$$P_{e,\text{seg-OSD}}(\tau_1, \tau_2) \leq P_{e,\text{ML}} + P_{\underline{e}_1}(\tau_1) + P_{\underline{e}_2}(\tau_2) - P_{\underline{e}_1}(\tau_1)P_{\underline{e}_2}(\tau_2). \quad (26)$$

Hence, if  $\tau_1 \geq \min \{ \lceil d/4 - 1 \rceil, k \}$ ,  $P_{\underline{e}_1}(\tau_1) \ll P_{e,\text{ML}}$ , and

$$P_{e,\text{seg-OSD}}(\tau_1, \tau_2) \leq P_{e,\text{ML}} + P_{\underline{e}_2}(\tau_2). \quad (27)$$

Therefore, if  $\tau_1 \geq \min \{ \lceil d/4 - 1 \rceil, k \}$  and  $\tau_2$  is appropriately chosen such that  $P_{\underline{e}_2}(\tau_2) \ll P_{e,\text{ML}}$ , the ML decoding performance can still be approached by the segmented OSD. This segmented variant helps reduce the number of TEPs

significantly, resulting in a reduced decoding complexity.

Note that the partition point in the MRPs can be more flexibly adjusted to achieve a better complexity reduction. But this process remains heuristic. More numerical results on this will be provided in Section VI.

## V. COMPLEXITY ANALYSIS

This section analyzes the complexity of the proposed OSD and compares it with the conventional OSD. In the conventional OSD, binary operations and floating point operations are needed. The GE process requires  $n \cdot (\min\{n-k, k\})^2$  binary operations. Based on  $\mathbf{G}''$ ,  $k \cdot (n-k)$  and  $(n-k) \cdot \sum_{\lambda=1}^{\tau} \lambda \binom{k}{\lambda}$  binary operations are needed to compute  $\hat{\underline{c}}^{(0)}$  and the other candidate codewords  $\hat{\underline{c}}^{(\omega)}$ , respectively. Finally, identifying the decoding output  $\hat{\underline{c}}_{\text{opt}}$  requires at most  $n \cdot \sum_{\lambda=0}^{\tau} \binom{k}{\lambda}$  floating point operations. In the proposed OSD, the  $\mathbb{F}_{2^m}$  finite field operations and floating point operations are needed. In computing the RS systematic generator matrix  $\mathbf{G}_{\text{RS}}$  as in (14) or (15),  $2n \cdot \min\{n-k', k'\}$  finite field operations are needed. The generation of  $\hat{\underline{v}}^{(0)}$  as in (16) requires at most  $k' \cdot (n-k')$  finite field operations. Let  $N_{j'}$  denote the number of TEPs  $\underline{e}^{j'(\omega)}$  that yield binary estimated symbols  $\hat{\underline{v}}_{j'}^{(\omega)}$  in  $\Theta^c$  after the  $j'$ 'th judgement as in *Theorem 2*, where  $j' = 0, 1, \dots, n-k'$ . Note that when  $j' = 0$ , no assessment has been conducted, and  $N_0$  is the total number of TEPs, i.e.,  $N_0 = \sum_{\lambda=0}^{\tau} \binom{k'}{\lambda}$ . A BCH codeword will be confirmed after the  $n-k'$  positions in  $\Theta^c$  have been assessed. Hence, the decoding output list cardinality of the proposed OSD is  $N_{n-k'}$ . Computing BCH codeword candidates  $\hat{\underline{v}}^{(\omega)}$  as in (22) requires at most  $\sum_{\lambda=1}^{\tau} \lambda \binom{k'}{\lambda} + \tau \sum_{j'=1}^{n-k'} N_{j'}$  finite field operations. Finally, identifying  $\hat{\underline{v}}_{\text{opt}}$  requires at most  $nN_{n-k'}$  floating point operations. The above complexity characterizations are summarized as in Table I. It can be seen that complexity of the proposed OSD also depends on  $N_{j'}$ . More numerical results will be provided in the following section, providing more insight of it.

TABLE I  
COMPLEXITY OF THE PROPOSED AND THE CONVENTIONAL OSDs.

Algorithms	Operations	Complexity
OSD ( $\tau$ )	GE	$n \cdot (\min\{n-k, k\})^2$
	Compute $\hat{\underline{c}}^{(0)}$	$k \cdot (n-k)$
	Compute $\hat{\underline{c}}^{(\omega)}$	$(n-k) \cdot \sum_{\lambda=1}^{\tau} \lambda \binom{k}{\lambda}$
	Find $\hat{\underline{c}}_{\text{opt}}$	$n \cdot \sum_{\lambda=0}^{\tau} \binom{k}{\lambda}$
Low-Lat. OSD ( $\tau$ )	Compute $\mathbf{G}_{\text{RS}}$	$2n \cdot \min\{n-k', k'\}$
	Compute $\hat{\underline{v}}^{(0)}$	$k' \cdot (n-k')$
	Compute $\hat{\underline{v}}^{(\omega)}$	$\sum_{\lambda=1}^{\tau} \lambda \binom{k'}{\lambda} + \tau \sum_{j'=1}^{n-k'} N_{j'}$
	Find $\hat{\underline{v}}_{\text{opt}}$	$nN_{n-k'}$

## VI. SIMULATION RESULTS

## A. Decoding Performance

Figs. 1 and 2 show the decoding frame error rate (FER) of the (31, 21) and the (63, 45) BCH codes, respectively.

For the segmented low-latency OSD, it is parameterized by  $(\tau_1 | l, \tau_2)$ , where  $l$  denotes the length of the first segment. That says  $\underline{e}'_1^{(\omega)} = (e'_{j_0}^{(\omega)}, e'_{j_1}^{(\omega)}, \dots, e'_{j_{l-1}}^{(\omega)})$  and  $\underline{e}'_2^{(\omega)} = (e'_{j_l}^{(\omega)}, e'_{j_{l+1}}^{(\omega)}, \dots, e'_{j_{k'-1}}^{(\omega)})$ . Performance of the Berlekamp-Massey (BM) decoding [15] and the conventional OSD [3] are presented as benchmarks. The ML decoding performances were obtained in [16]. Our results show that the low-latency OSD performance can approach that of the conventional OSD, but requires a larger decoding order. This is due to the fact that  $k' > k$  and  $|\Theta| > |\Upsilon|$ , more errors will be introduced in the MRPs of the low-latency OSD. However, our results also show that the segmented variant can yield a similar decoding performance with a smaller order.

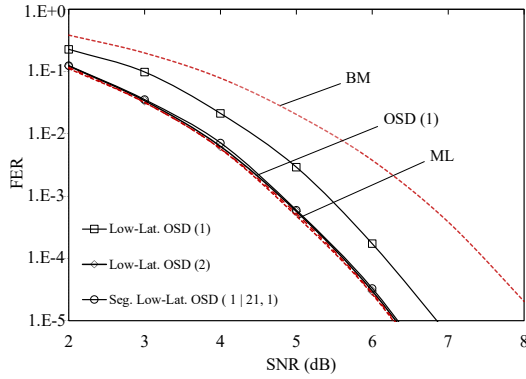


Fig. 1. Performance of the (31, 21) BCH code.

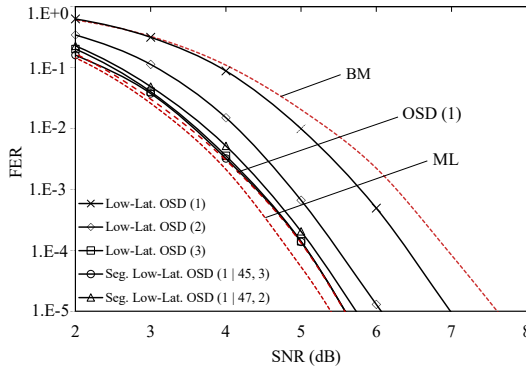


Fig. 2. Performance of the (63, 45) BCH code.

### B. Decoding Complexity and Latency

As pointed out in Section V, complexity of the proposed OSD depends on  $N_{j'}$ . Table II shows our numerical results of  $N_{j'}$  in decoding the (63, 45) BCH code with  $\tau = 3$ . Note that the BCH code is a binary subcode of the (63, 57) RS code. It can be seen that the assessment of *Theorem 2* can effectively eliminate the nonbinary codewords. E.g., after assessing the first symbol in  $\Theta^c$ , i.e.,  $\hat{v}_{j_{k'}}^{(\omega)}$ , there are only 957 TEPs that can possibly produce BCH codewords. Moreover, the decoding output list cardinality  $N_6$  is only 7, which is far smaller than that of the conventional OSD with  $\tau = 1$ . This will result in

the complexity advantage of the proposed OSDs, as discussed below.

TABLE II  
NUMERICAL RESULTS OF  $N_{j'}$  IN DECODING THE (63, 45) BCH CODE WITH  $\tau = 3$ .

$j'$	0	1	2	3	4	5	6
$N_{j'}$	30914	957	36	9	8	7	7

TABLE III  
NUMERICAL RESULTS OF COMPLEXITY AND LATENCY IN DECODING THE (63, 45) BCH CODE.

Algorithms	SNR (dB)	Complexity		Latency ( $\mu$ s)
		$\mathbb{F}_2/\mathbb{F}_{64}$ oper.	Floating oper.	
OSD (1)	4	$2.78 \times 10^4$	81	$6.58 \times 10^2$
	5	$2.60 \times 10^4$	19	$5.34 \times 10^2$
	6	$2.56 \times 10^4$	8	$5.06 \times 10^2$
Low-Lat. OSD (3)	4	$1.81 \times 10^4$	15	$1.99 \times 10^3$
	5	$5.21 \times 10^3$	8	$4.36 \times 10^2$
	6	$2.58 \times 10^3$	7	$1.32 \times 10^2$
Seg. Low-Lat. OSD (1   45, 3)	4	$3.69 \times 10^3$	8	$2.71 \times 10^2$
	5	$2.64 \times 10^3$	7	$1.44 \times 10^2$
	6	$2.45 \times 10^3$	7	$1.17 \times 10^2$

Table III compares the complexity and latency in decoding the (63, 45) BCH code. All OSDs will terminate once an ML codeword is identified by (9). The decoding complexity and latency are measured and averaged as in decoding one codeword. Referring to Fig. 2, to achieve the same decoding performance, the number of finite field operations in the two proposed OSDs are smaller than that of the binary operations in the conventional OSD, especially segmented variant. Despite the proposed OSDs incur more TEPs, Table II shows that the binary codeword assessment of *Theorem 2* helps eliminate the redundant ones effectively, resulting in a relatively low level of finite field operations. This assessment also results in fewer floating point operations required by the ML criterion. Finally, Table III also vindicates the latency advantage of the proposed OSDs. Our simulations were performed with the Intel core i7-10710U CPU. In the proposed OSDs, each row of  $\mathbf{G}_{RS}$  is generated in parallel. In all OSDs, the TEPs are decoded in a serial manner. It can be seen that both the low-latency OSD and its segmented variant can effectively reduce the decoding latency over the conventional OSD, vindicating the latency advantage of our proposed OSDs.

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